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THE ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231

DOCKET NUMBER: AUS920030711US1

DATE: July 10, 2003

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventors: Sang Hoo Dhong, Hwa-Joon Oh, Joel Abraham Silberman, Naoka Yano

For: Integrated Logic And Latch Design With Clock Gating At Static Input Signals

Assignee Name: International Business Machines Corporation

Assignee Residence: Armonk, New York

Assignee Name: Toshiba America Electronic Components, Inc

Assignee Residence: Irvine, California

Assignee Name: Kabushiki Kaisha Toshiba

Assignee Residence: Tokyo, Japan

21904 U.S. PTO
10/616850
07/10/03

Enclosed are:

- X 22 Patent Specification including an Abstract
X 9 Pages of Claims
X 4 Sheet(s) of drawings. Informal X Formal
X 3 Declaration(s) and Power of Attorney
X Form PTO 1595 and Assignment of the Invention to IBM Corporation
X Form PTO 1595 and Assignment of the Invention to Toshiba America Electronic Components
X Form PTO 1595 and Assignment of the Invention to Kabushiki Kaisha Toshiba
 A certified copy of a application.
 Information Disclosure Statement, PTO 1449 and copies of references.

The filing fee has been calculated as shown below:

FOR	Number Filed		Number Extra		Rate		Basic Fee \$750.00
Total Claims	27	-20 =	7	X	\$ 18	=	\$ 126.00
Independent Claims	4	3 =	1	X	\$ 84	=	\$ 84.00
Multiple Dependent Claims	00			X	\$280	=	
Total Filing Fee							= \$ 960.00

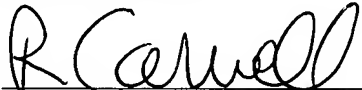
X Please charge my Deposit Account No. 09-0447 in the amount of \$ 960.00 . A duplicate copy of this sheet is enclosed.

X The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0447 . A duplicate copy of this sheet is enclosed.

X Any additional filing fees required under 37 CFR 1.16.

X Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

By 

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